

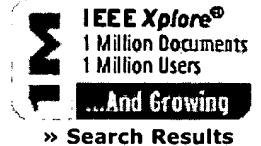
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4	52	fault and (data and control and power) same host same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:27
5	10	fault same (data and control and power) same host same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:28
6	50	fault same host same emulat\$ and (data and control and power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:29
7	7	fault with host with emulat\$ and (data and control and power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:29
8	0	fault with host with emulat\$ same (data and control and power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:30
9	12	fault with host with emulat\$ same (data control power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:33
10	11	fault with computer with emulat\$ same (data control power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 11:13
19	117	(dynamic plug and play automatic\$) with (host computer) with emulat\$ same (data control power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:37
20	44	(dynamic plug and play) with (host computer) with emulat\$ same (data control power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:38
21	44	dynamic with (host computer) with emulat\$ same (data control power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:38

22	0	(plug and play) with (host computer) with emulat\$ same (data control power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:37
23	56	dynamic\$ with (host computer) with emulat\$ same (data control power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:38
24	25	dynamic\$ with host with emulat\$ same (data control power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:39
25	1	dynamic\$ with connect\$ with host with emulat\$ same (data control power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:40
26	1	dynamic\$ with connect\$ with host with emulat\$ and (data and control and power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:40
27	4	dynamic\$ with connect\$ same host with emulat\$ and (data and control and power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:42
30	1	dynamic\$ with (connect\$3 disconnect\$3) and host with emulat\$ same (data and control and power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:44
31	10	dynamic\$ with (connect\$3 disconnect\$3) and host same emulat\$ same (data and control and power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:46
32	134	dynamic\$ with (connect\$3 disconnect\$3) and host same emulat\$ same (data control power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:46
33	10	dynamic\$ with (connect\$3 disconnect\$3) and host same emulat\$ same ((data control) and power) and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:47
34	12	dynamic\$ with (connect\$3 disconnect\$3) same host same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:56

35	39	hot same host same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:50
36	4	hot with host with emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:52
37	10	hot\$ with host with emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:53
38	9	swap\$ with host with emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:54
39	0	(plug and play) with host with emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:55
40	4	(plug and play) same host with emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:55
41	22	automatic\$ with (connect\$3 disconnect\$3) same host same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:56
48	30	fault with computer with emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 11:14
-	692	703/23 and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:06
-	5	703/23 and (power and control) near data and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:17
-	1	703/23 and (user and power and control) near data and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:15

-	1	703/24 and (user and power and control) near data and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:15
-	1	703/25 and (user and power and control) near data and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:15
-	2	710/62 and (user and power and control) near data and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:16
-	1	718/\$ and (user and power and control) near data and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:17
-	109	718/\$ and (user power control) near data and emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:17
-	27	718/\$ and (user power control) near data and emulat\$ same host and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:18
-	6	718/\$ and (user power control) near data same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:26
-	51	703/23 and (user power control) near data same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:27
-	1	703/23 and ((user control) and power) near data same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:31
-	0	703/24 and ((user control) and power) near data same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:28
-	0	703/25 and ((user control) and power) near data same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:28

-	1	703/\$ and ((user control) and power) near data same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:30
-	1	((user control) and power) near data same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:30
-	69	703/23 and ((data control) and power) same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:31
-	59	703/23 and (data and control and power) same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:32
-	81	703/23 and (data and control and power) and host and emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 13:32
-	9	703/23 and (data and control and power) same host same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 14:13
-	5	703/24 and (data and control and power) same host same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 14:16
-	5	703/25 and (data and control and power) same host same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 14:18
-	26	703/\$ and (data and control and power) same host same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 14:35
-	8	(714/28 714/29) and (data and control and power) same host same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/10 14:36
-	1	(717/134 717/138) and (data and control and power) same host same emulat\$ and (@ad<20001004 @rlad<20001004)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/11 10:26

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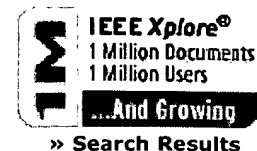
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Computers, IEEE Transactions on , Volume: 45 , Issue: 8 , Aug. 1996

Pages:881 - 891

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### 1 Experiences with the application of LAN emulation in a data acquisition system

*De Laa, C.T.A.M.; Kuijter, P.G.; Olthuis, H.P.; Giesing, V.J.; Venema, J.;*  
Nuclear Science, IEEE Transactions on , Volume: 44 , Issue: 4 , Aug. 1997  
Pages:1635 - 1638

[\[Abstract\]](#)   [\[PDF Full-Text \(56 KB\)\]](#)   IEEE JNL

### 2 Distributed computation of wave propagation models using PVM

*Ewing, R.E.; Sharpley, R.C.; Mitchum, D.; O'Leary, P.; Sochacki, J.S.;*  
Parallel & Distributed Technology: Systems & Applications, IEEE [see also IEEE  
Concurrency] , Volume: 2 , Issue: 1 , Spring 1994  
Pages:26 - 31

[\[Abstract\]](#)   [\[PDF Full-Text \(444 KB\)\]](#)   IEEE JNL

### 3 CONDOR: an architecture for controlling the Utah-MIT dexterous hand

*Narasimhan, S.; Siegel, D.M.; Hollerbach, J.M.;*  
Robotics and Automation, IEEE Transactions on , Volume: 5 , Issue: 5 , Oct. 1989  
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[\[Abstract\]](#)   [\[PDF Full-Text \(1180 KB\)\]](#)   IEEE JNL

### 4 Bypassing the CAMAC data bus to read out FERA data at higher rates

*Siegel, S.; Vaquero, J.J.; Seidel, J.; Gandler, W.R.; Green, M.V.;*  
Nuclear Science Symposium, 1998. Conference Record. 1998 IEEE , Volume: 3 , 8-  
14 Nov. 1998  
Pages:1461 - 1462 vol.3

[\[Abstract\]](#)   [\[PDF Full-Text \(160 KB\)\]](#)   IEEE CNF

### 5 Real-time computing of special algorithms with a DSP-based board

*Aiello, S.; Anzalone, A.; Bartolucci, M.; Cardella, G.; Cavallaro, S.; De Filippo, E.;*  
*Di Pietro, A.; Femino, S.; Geraci, M.; Guazzoni, P.; Manno, M.I.; Lanzalone, G.;*

Lanzano, G.; Lo Nigro, S.; Manfredi, G.; Musumarra, A.; Pagano, A.; Papa, M.; Pirrone, S.; Politi, G.; Porto, F.; Rizzo, F.; Sambataro, S.; Sechi, G.; Sperduto, L.; Sutura, C.; Zetta, L.;  
Real-Time Systems, 1996., Proceedings of the Eighth Euromicro Workshop on , 12-14 June 1996  
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**6 Development and prototyping system far an 8-bit multitask micropower processor**

Fink, S.; Sanchez, E.;  
Rapid System Prototyping, 1995. Proceedings., Sixth IEEE International Workshop on , 7-9 June 1995  
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**7 Software system for ABC-90jr.-an array based computer**

Guansong Zhang; Binxing Fang; Xiaoming Li;  
TENCON '93. Proceedings. Computer, Communication, Control and Power Engineering.1993 IEEE Region 10 Conference on , Issue: 0 , 19-21 Oct. 1993  
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Robotics and Automation, 1988. Proceedings., 1988 IEEE International Conference on , 24-29 April 1988  
Pages:446 - 449 vol.1

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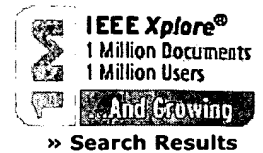
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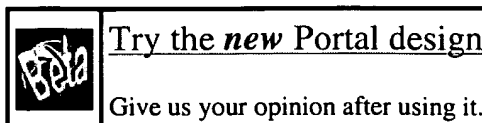
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Tajana Simunic , Haris Vikalo , Peter Glynn , Giovanni De Micheli  
**Proceedings of the 2000 international symposium on Low power electronics and design** August 2000  
 Portable wireless systems require long battery lifetime while still delivering high performance. The major contribution of this work is combining new it power management(PM) and it power control (PC) algorithms to trade off performance for power consumption at the system level in portable devices. First we present the formulation for the solution of the PM policy optimization based on renewaltheory. Next we present the formulation for power control (PC) of ...
- 5** The broadcast storm problem in a mobile ad hoc network 77%

Sze-Yao Ni , Yu-Chee Tseng , Yuh-Shyan Chen , Jang-Ping Sheu  
**Proceedings of the 5th annual ACM/IEEE international conference on Mobile computing and networking** August 1999
- 6** Adaptive hybrid clock discipline algorithm for the network time protocol 77%

 David L. Mills  
**IEEE/ACM Transactions on Networking (TON)** October 1998  
Volume 6 Issue 5

**7** Agile application-aware adaptation for mobility 77%

 Brian D. Noble , M. Satyanarayanan , Dushyanth Narayanan , James Eric Tilton , Jason Flinn , Kevin R. Walker  
**ACM SIGOPS Operating Systems Review , Proceedings of the sixteenth ACM symposium on Operating systems principles** October 1997  
Volume 31 Issue 5

**8** Ada implementation of a real-time communications system 77%


 Thomas J. Brady  
**Proceedings of the conference on TRI-ADA '90** December 1990

**9** The SpectrumWare approach to wireless signal processing 77%


 David L. Tennenhouse , Vanu G. Bose  
**Wireless Networks** March 1996  
Volume 2 Issue 1

The SpectrumWare project is applying a software oriented approach to wireless communication and distributed signal processing. Advances in processor and analog-to-digital conversion technology have made it possible to implement virtual radios that directly sample wide bands of the RF spectrum and process these samples in application software. The elimination of dedicated hardware introduces tremendous flexibility into a wireless communication system. Our approach goes further than the softw ...

**10** SpectrumWare: a software-oriented approach to wireless signal processing 77%

 David L. Tennenhouse , Vanu G. Bose  
**Proceedings of the 1st annual international conference on Mobile computing and networking** December 1995

**11** Pen computing: a technology overview and a vision 77%

 André Meyer  
**ACM SIGCHI Bulletin** July 1995  
Volume 27 Issue 3

This work gives an overview of a new technology that is attracting growing interest in public as well as in the computer industry itself. The visible difference from other technologies is in the use of a pen or pencil as the primary means of interaction between a user and a machine, picking up the familiar pen and paper interface metaphor. From this follows a set of consequences that will be analyzed and put into context with other emerging technologies and visions. Starting with a short historic ...

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**Results 1 - 11 of 11      short listing**

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